

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of the Claims:

1. (Currently Amended) A power supply circuit for a digital processing system, the circuit comprising:

a first stage having a first output coupled to a first component of the digital processing system and a second output which is different from the first output, wherein said second output has an output winding;

a second stage associated with a second component of the digital processing system, and having an input winding, wherein said input winding of said second stage is coupled connected to said output winding of said first stage through a two wire bus; and

wherein said first stage drives said second stage using the second output, wherein the second stage transforms the second output to generate a third output to drive the second component, and wherein the first output is independent of the second stage.
2. (Original) The circuit of claim 1, wherein said first and second stages are separated from each other.
3. (Cancelled)
4. (Currently Amended) The circuit of claim [[3]] 1, wherein said two wire bus is differentially driven by said first stage.

5. (Original) The circuit of claim 2, wherein said first component comprises a display device and said second component comprises a microprocessor.
6. (Original) The circuit of claim 5, wherein said first stage is located proximately to said display device and said second stage is located proximately to said microprocessor.
7. (Original) The circuit of claim 1, wherein said first stage provides power for said first component and said second stage provides power for said second component.
8. (Original) The circuit of claim 7, wherein said first stage comprises a flyback converter and said second stage comprises a portion of a forward converter.
9. (Currently Amended) A power supply circuit for a computer system, the circuit comprising:
  - a first circuit having a first output capable of providing power to a first component of the computer system and a second output which is different from the first output, wherein said second output has an output winding; and
  - a second circuit having an input winding and capable of providing power to a second component of the computer system, wherein said input winding of said second circuit is connected to said output winding of said first circuit through a two wire bus;
  - wherein said first circuit drives the second circuit through the second output, wherein the second circuit transforms the second output to generate a third output to drive the second component, and wherein the first output is independent of the second circuit.

10. (Cancelled)

11. (Previously Presented) The circuit of claim 9, wherein said second circuit and said second component are disposed on a printed circuit board.

12. (Original) The circuit of claim 9, wherein said first circuit is located within an enclosure of the computer system and proximately to said first component, and wherein said second circuit is located within said enclosure and proximately to said second component.

13. (Original) The circuit of claim 12, wherein said first component comprises a display device and said second component comprises a microprocessor.

14. (Original) The circuit of claim 9 wherein said first circuit comprises a flyback converter and said second circuit comprises a final stage of a forward converter.

15. (Currently Amended) A computer system comprising:

a power supply circuit coupled to a display device and a microprocessor of the computer system, wherein said power supply circuit is capable of supplying power to said display device and said microprocessor using at least two distinct power supply stages;

a main circuit having a first output and a second output, wherein said main circuit is coupled to said display device using a first output; and

a secondary circuit coupled to said microprocessor, said secondary circuit having an input winding, wherein said second output is connected to said input winding through a two wire bus; and

wherein said main circuit drives said secondary circuit using a second output which is different from the first output, wherein said secondary circuit transforms said second output to generate a third output to drive the microprocessor, and wherein the first output is independent of the secondary circuit.

16. (Cancelled)

17. (Previously Presented) The computer system of claim 15, wherein one of said at least two distinct power supply stages includes said main circuit, and wherein another of said at least two distinct power supply stages includes said secondary circuit.

18. (Previously Presented) The computer system of claim 15, wherein said main circuit and said secondary circuit are physically isolated from each other.

19. (Original) The computer system of claim 18, wherein said main circuit and said secondary circuit are electrically coupled to each other.

20. (Previously Presented) The computer system of claim 15, wherein said main circuit comprises a flyback converter and said secondary circuit comprises a portion of a forward converter.

21. (Previously Presented) The power supply circuit of claim 1, wherein said first output provides a direct (DC) voltage, and wherein said second output provides an alternating current (AC) voltage.

22. (Previously Presented) The power supply circuit of claim 9, wherein said first output provides a direct current (DC) voltage and said second output provides an alternating current (AC) voltage.